

		Application Number	09/234,233		
TRANSMITTAL FORM (to be used for all correspondence after initial filing)		Filing Date		January 20, 1999	
		First Named Inventor	Weimin Li		
		Group Art Unit	2818		
		Examiner Name	D. Vu		
Total Number of Pages in This Submission		Attorney Docket Number MI22-1035			
	ENCLO	SURES (check all that ap	ply)		
Fee Transmittal Form	Assignn	nent Papers Application)		e Communication	
Fee Attached	Drawing)(s)		unication to Board d Interferences	
Amendment / Response	Licensing-related Papers		Appeal Comm	unication to Group e/, Raphy Bray)	
After Final	Petition Routing Slip (PTO/SB/69) Proprietary Information			formation	
Affidavits/declaration(s)	Provisio	to Convert to a nal Application	Status Letter		
Extension of Time Request	Change Address	of Attorney, Revocation of Correspondence	X Additional End (please Identi		
Express Abandonment Request		l Disclaimer	Return Postcard PTO-1449; Cited	Reference	
X Information Disclosure Statemen	∮ ≓	ntity Statement			
Certified Copy of Priority Document(s)	Reques	t for Refund			
Response to Missing Parts/ Incomplete Application	The Commissioner	Customer No. 021567 This is hereby authorized to charge a 1.17 and credit any overpayment	any additional fees requir	ed under 37 CFR	
Response to Missing Parts under 37 CFR 1.52 or 1.53		ALLY MIN WHOLE MAY OVER PHYSICAL		RECEIV	
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Firm or James E. Lake Individual name Reg. No. 44,854	l; Wells St. J	ohn P.S.	TECHNO	OLOGY CENTE	
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Date 16 i	Jan 2003				
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34/I.D.S.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/234,233
Filing Date	January 20, 1999
Inventor	
Assignee	
Group Art Unit	
Examiner	D. Vu
Attorney's Docket No.	MI22-1035
Title: Semiconductor Processing Method	The state of the s

INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed after the filing of the Request for Continued Examination (RCE) Application and before receipt of the first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted RECEIVED

JAN 1 6 2003

Dated: 16 Jan 2003

Attorney:

TECHNOLOGY CENTER 2800

Reg. No.: 44,854



P.01/18

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	09/234,233
Filing Date	
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	D. Vu
Attorney Docket No	
Title: Semiconductor Processing Methods	FAX RECEIVED

JAN 1 6 2003

Assistant Commissioner for Patents Washington, D.C. 20231

TECHNOLOGY CENTER 2800

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9318 on the date shown below:

- 1. Certificate of Facsimile Transmission
- 2. Transmittal form PTO/SB/21
- 3. Information Disclosure Statement; PTO-1449, cited reference

Dated:

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